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Title of Invention	LOW LEAKAGE MONOTONIC CMOS LOGIC

Submision Type :   Utility Patent Filing

Application Number:

                                 10/710453

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First Named Applicant: Kerry Bernstein

Attorney Docket Number: BUR920040024US1

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us-fee-sheet	us-fee-sheet.dtd	11069	2004-07-13
us-assignment	BUR920040024US1-usassn.xml	1888	2004-07-13
us-assignment	us-assignment.xsl	8983	2004-07-13
us-assignment	us-assignment.dtd	10898	2004-07-13
us-assignment	BUR920040024US1assign1.tif	95540	2004-07-13
us-assignment	BUR920040024US1assign2.tif	15208	2004-07-13
us-request	BUR920040024US1-usrequ.xml	4318	2004-07-13
us-request	us-request.dtd	19064	2004-07-13
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application-body	us-application-body.xsl	83497	2004-07-13
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application-body	wipo.ent	4956	2004-07-13
application-body	mathml2.dtd	54588	2004-07-13
application-body	mathml2-qname-1.mod	13225	2004-07-13
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application-body	isoamsb.ent	3988	2004-07-13
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application-body	isoamsn.ent	5620	2004-07-13
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application-body	BUR920040024US1drawing10.tif	20144	2004-07-13
us-declaration	BUR920040024US1declaration1.tif	73928	2004-07-13
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package-data	BUR920040024US1-pkda.xml	10297	2004-07-13
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